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Message:


Applicant(s)	Efren M. Lacap et al.	Examiner	Jonathan J. Johnson
Serial No.	10/648,586	Group Art No.	1725
Filed	26 August 2003	Confirmation No.	4089
For	WAFER-LEVEL CHIP SCALE PACKAGE		

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Applicant(s): Efren M. Lacap, et al.			408204
Serial No.	Filing Date	Examiner	Group Art Unit
10/648,586	August 26, 2003	Jonathan J. Johnson	1725
Invention Wafer-Level Chip Scale Package			
<p>I hereby certify that the following: Facsimile Cover Sheet (1 page); Request For Continued Examination (RCE) Transmittal (1 page in duplicate); Amendment And Response (15 pages); authorization to charge Deposit Account No. 12-0600 in the amount of \$395.00.00 for the RCE filing fee; authorization to charge additional fees that may be required, or credit any overpayment, to Deposit Account No. 12-0600; are being sent via facsimile transmission to Mail Stop: RCE, c/o the Commissioner For Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 4th day of April, 2006</p>			
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